2621

E J			Applic	cation Number	10/010,674	
ໝ ິ ຣ∕TRANSMITTAL			Filing	Date	November 30, 2001	
	FORM	,	First I	Named Inventor	Chatterjee, Chanchal	
(ico) used for all co	rrespondence after ir	nitial filing)	Group	Art Unit	2621	
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Total Number of Pages	s in This Submission	n 4	Attorn	ey Docket Number	018926-008900US	
		ENCL'	OSURES	(check all that apply)		
Fee Transmittal F	-corm	Assigni	Assignment Papers (for an Application)		After Allowance Communication to Group	
Fee Attached	ı	☐ Drawin	Drawing(s)		Appeal Communication to Board of Appeals and Interferences	
Amendment / Res	sponse		sing-related		Appeal Communication to Group (Appeal Notice, Brief, Reply Brief)	
After Final		Petition Routing Slip (PTO/SB/69) and Accompanying Petition			Proprietary Information	
Affidavits/dec	aration(s)		Petition to Convert to a Provisional Application		Status Letter	
Extension of Time	e Request		Power of Attorney, Revocation Change of Correspondence Address		Other Enclosure(s) (please identify below):	
Express Abandon	nment Request		☐ Terminal Disclaimer ☐ Request for Refund		Return Postcard	
Information Disclo	osure Statement		umber of (
Certified Copy of Document(s)	Priority		Remarks The Commissioner is Deposit Account 20-1		authorized to charge any additional fee. 430.	
Response to Miss						
Response to Parts under 3 1.52 or 1.53	Missing			·	·	
				ANT, ATTORNEY, O	R AGENT	
Firm and Individual name	Townsend and Townsend and Coulomb Gregory E. Stanton				o. 45,127	
Signature	15.	t				
Date	May 3, 2002					

Typed or printed name Landon Clark

Signature Date May 3, 2002

class mail in an envelope addressed to: Assistant Commissioner for Patents, Washington, D.C. 20231 on this date:

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on May 3, 2002

TOWNSEND and TOWNSEND and CREW LLP

By: Jandon Clark

Attorney Docket No.: 018926-008900US
Client Reference No.: D2770



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of:

CHATTERJEE

Application No.: 10/010,674

Filed: November 30, 2001

For: SYSTEMS AND METHODS FOR

EFFICIENT QUANTIZATION

Examiner: U

Unassigned

Art Unit:

2621

INFORMATION DISCLOSURE

STATEMENT UNDER 37 CFR §1.97 and

§1.98

Assistant Commissioner for Patents Washington, D.C. 20231

Sir:

The references cited on attached form PTO/SB/08A and PTO/SB/08B are being called to the attention of the Examiner. Copies of the references are enclosed. It is respectfully requested that the cited references be expressly considered during the prosecution of this application, and the references be made of record therein and appear among the "references cited" on any patent to issue therefrom.

As provided for by 37 CFR 1.97(g) and (h), no inference should be made that the information and references cited are prior art merely because they are in this statement and no representation is being made that a search has been conducted or that this statement encompasses all the possible relevant information.

CHATTERJEE

Application No.: 10/010,674

Page 2

Applicant believes that <u>no fee is required</u> for submission of this statement, since it is being submitted prior to the first Office Action. However, if a fee is required, the Commissioner is authorized to deduct such fee from the undersigned's Deposit Account No. 20-1430. Please deduct any additional fees from, or credit any overpayment to, the above-noted Deposit Account.

Respectfully submitted,

Gregory E. Stanton Reg. No. 45,127

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GES/llc

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Substitute for form 1449B/PTO

INFORMATION DISCLOSURE STATEMENT BY APPLICANT

Complete if Known

Application Number 10/010,674

Filing Date November 30, 2001

First Named Inventor Chatterjee, Chanchal

Art Unit 2621

Examiner Name Unassigned

Attorney Docket Number 018926-008900US

(use as many sheets as necessary)

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	1	OTHER PRIOR ART NON PATENT LITERATURE DOCUMENTS				
Examiner nitials *	Cite No.1	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.				
	А	"IA-32 Intel® Architecture Software Developer's Manual, Vol. 1: Basic Architecture", Intel Corporation, P.O. Box 7641, Mt. Prospect IL 60056-7641, pp. iii to xii, 10-1 to 10-20, 11-1 to 11-36, (2002)				
	В	"IA-32 Intel® Architecture Software Developer's Manual, Vol. 2: Instruction Set Reference", Intel Corporation, P.O. Box 7641, Mt. Prospect IL 60056-7641, pp. iii to xiii, 3-545 to 3-547, 3-578 to 3-580, 3-639 to 3-641, (2002)				
	С	"Information technology - Coding of moving pictures and associated audio for digital storage media at up to about 1,5 Mbit/s - Part 2: Video", ISO/IEC Copyright Office, Case Postale 56, CH1211 Genève 20, Switzerland, pp. ii, 1-17, 51-57, 77-105, (1993)				
	D	"VR5432 64-Bit MIPS® RISC Microprocessor", Vol. 2, NEC Electronics Inc., U.S.A., pp. iii-xiii, 677-683, (2000)				
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Examiner	Date	, _		
Signature	Considered			

^{*}EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

¹ Applicant's unique citation designation number (optional). 2 Applicant is to place a check mark here if English language Translation is attached.



